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IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re Application of:

W. Fu et al

Serial No.: 10/823,060

Filed: 04/13/2004

For:

**HIGH SPEED LINEAR HALF RATE PHASE DETECTOR
ARCHITECTURE WITH INDEPENDENT CLOCK PHASE OFFSET AND
DC OUTPUT OFFSET CONTROL**



Confirmation No. 3987

Art Unit: 2816

Examiner: M.T. Nguyen

Docket No. AMCC10180

CERTIFICATION UNDER 37 CFR § 1.8

I hereby certify that the documents referred to as enclosed herein are being deposited with the United States Postal Service as first class mail on this date September 22, 2005 in an envelope addressed to:
Commissioner for Patents, Box 1450, Alexandria, VA. 22313-1450

September 22, 2005 Tenance A. Neador
Date Signature

MAIL STOP: AMENDMENT
Commissioner for Patents
P. O. Box 1450
Alexandria, VA. 22313-1450

AMENDMENT AND REQUEST FOR RECONSIDERATION

In response to the Office Action dated 06/23/2005, Applicant requests amendment of the application as indicated below. All pending claims are included below.